

OLED-1 Peripheral Port Mapping to MINI-MAX Microcontroller Board Family

TB-1 Feature	Exp. Conn. Pin Number	Active When	Universal Pin Names	Exp. Conn. J4 51	Exp. Conn. X6 AVR-C	Exp. Conn. X6 AVR-AU	Exp. Conn. X8 AVR-BU	Exp. Conn. J3 ARM	Exp. Conn. X1 ARM9260	Exp. Conn. J4 P18	Exp. Conn. X3 MSP430
	1		GND	GND	GND	GND	GND	GND	GND	GND	GND
	2		VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)
	3		GND	GND	GND	GND	GND	GND	GND	GND	GND
	4		VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)	VCC (+5V)
	5		I2C SDA	P1.7	PD1	PC5	PD1	P0.3	PA23	RC4	P3.7
	6		I2C SCL	P1.6	PD0	PC6	PD0	P0.2	PA24	RC3	P5.4
	7		IO4	P1.5	PD6	PD1	PF7	P0.14	PC1	RA2	P2.4
	8		IO5	P1.4	PD7	PD4	PE7	P0.15	PA6	RC5	P2.5
	9		IO2	P1.3	PB6	PD0	PF6	P0.12	PB15	RA1	P2.2
	10		IO3	P1.2	PB7	PC4	PE6	P0.13	PB14	RA0	P2.3
	11		IO0	P1.1	PB4	PC7	PF5	P0.10	PB16	RC2	P2.0
	12		IO1	P1.0	PB5	PC2	PE5	P0.11	PB17	RB7	P2.1
Data In/D1	13		MOSI	P3.7	PB2	PB2	PB2	P0.6	PB1	RA5	P3.1
Chip Select	14		IO22	P3.6	PC7	PB7	PE4	P1.24	PA31	RC0	P2.7
Chip Select Alternative	15		SS	P3.5	PB0	PB0	PB0	P0.7	PB3	RA4	P3.0
Clock/D0	16		SCK	P3.4	PB1	PB1	PB1	P0.4	PB2	RC1	P3.3
	17		MISO	P3.3	PB3	PB3	PB3	P0.5	PB0	RB3 or RB5	P3.2
DC	18		IO6	P3.2	PH2	PB6	PE1	P0.26	PA30	RB6	P2.6
	19		/TXD2	P3.1	PH1	PD3	PD3	P0.0	PB8	RC6	P1.6
	20		/RXD2	P3.0	PH0	PD2	PD2	P0.1	PB9	RC7	P1.5